

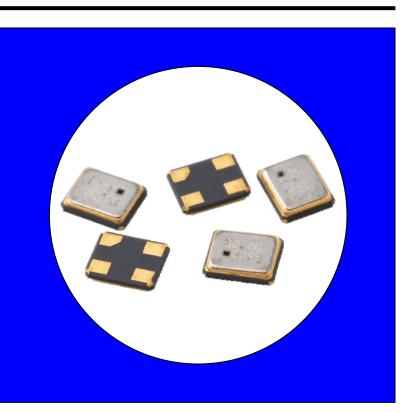


FEATURES

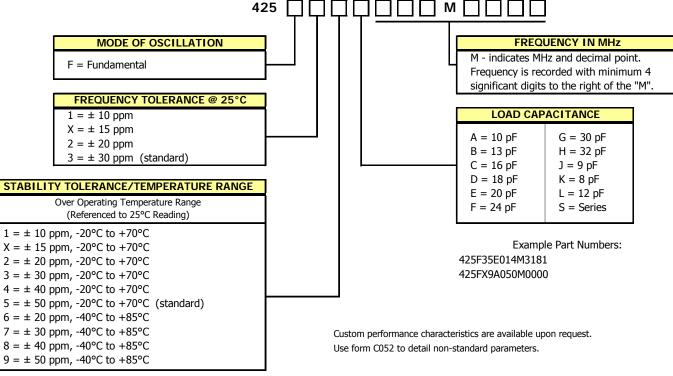
- Standard 2.5x2.0mm Surface Mount Footprint
- Stable Frequency Over Temperature and Drive Level
- Fundamental Crystal Design
- Frequency Range 16 54 MHz
- Frequency Tolerance, ±30 ppm Standard (±10 ppm, ±15 ppm and ±20 ppm available)
- Frequency Stability, ±50 ppm Standard (±10,±15,±20,±30 and ±40 ppm available)
- Operating Temperature to -40°C to +85°C
- Tape & Reel Packaging, EIA-481-2 Compliant
- RoHS/Green Compliant (6/6)

DESCRIPTION

The Model 425 is a ceramic packaged Crystal offering reduced size, ideal for high-density circuit board applications. The Model 425 offers reliable precision and excellent shock performance in wireless telecommunication devices.



ORDERING INFORMATION



Not all performance combinations and frequencies may be available. Contact your local CTS Representative or CTS Customer Service for availability.

SHENZHEN KANGBI TECHNOLOGY INDUSTRY CO;LTD

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ELECTRICAL CHARACTERISTICS

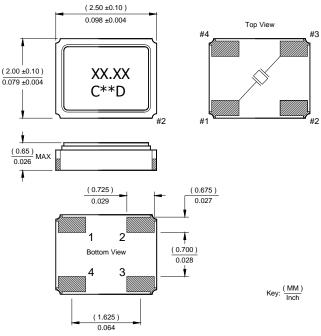
| | PARAMETER | VALUE | | | | | | | |
|-----------------------|---|--|--|--|--|--|--|--|--|
| | Operating Mode | Fundamental | | | | | | | |
| | Crystal Cut | AT-Cut | | | | | | | |
| | Frequency Range | 16.0 MHz to 54.0 MHz | | | | | | | |
| | Frequency Tolerance @ 25°C | ± 30 ppm Standard | | | | | | | |
| | Trequency Tolerance @ 25 C | $(\pm 10 \text{ ppm}, \pm 15 \text{ ppm and } \pm 20 \text{ ppm Available})$ | | | | | | | |
| ters | Frequency Stability Tolerance | ± 50 ppm Standard | | | | | | | |
| Electrical Parameters | (Operating Temperature Range, Referenced to 25°C Reading) | (\pm 10 ppm, \pm 15 ppm, \pm 20 ppm, \pm 30 ppm and \pm 40 ppm Available) | | | | | | | |
| | Operating Temperature Range | -20°C to +70°C Standard | | | | | | | |
| | Operating reinperature Range | (-40°C to +85°C Available) | | | | | | | |
| ctri | Storage Temperature Range | -40°C to +85°C | | | | | | | |
| Ele | Equivalent Series Resistance | See ESR Table | | | | | | | |
| | Load Capacitance or Resonance Mode | See Ordering Information | | | | | | | |
| | Shunt Capacitance (C_0) | 5.0 pF Maximum | | | | | | | |
| | Drive Level | 10 μW Typical, 100 μW Maximum | | | | | | | |
| | Aging @ 25°C | ± 3 ppm/year maximum | | | | | | | |
| | Reflow Condition, per JEDEC J-STD-020 | +255°C ± 5°C, 10 Seconds Maximum | | | | | | | |

EQUIVALENT SERIES RESISTANCE TABLE

| FREQUENCY RANGE | MODE of OSCILLATION | ESR Maximum |
|------------------------|---------------------|-------------|
| 16.00 MHz - 19.999 MHz | Fundamental | 100 Ohms |
| 20.00 MHz - 29.999 MHz | Fundamental | 80 Ohms |
| 30.00 MHz - 39.999 MHz | Fundamental | 80 Ohms |
| 40.00 MHz - 54.000 MHz | Fundamental | 60 Ohms |

MECHANICAL SPECIFICATIONS

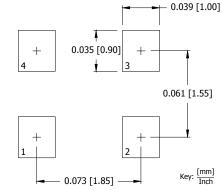
PACKAGE DRAWING



MARKING INFORMATION

- 1. XX.XX Frequency in MHz.
- 2. C CTS and Pin 1 identifier.
- 3. ** Manufacturing Site Code.
- 4. D Manufactured Date Code. See Table I for codes.
- 5. Complete CTS part number, frequency value and date code information must appear on reel and box labels.

SUGGESTED SOLDER PAD GEOMETRY



Notes:

- 1. Termination pads (e4), barrier-plating is nickel (Ni) with gold (Au) flash plate.
- 2. Terminations #2, #4 and the metal lid are connected internally. End user may connect these pins to circuit ground.

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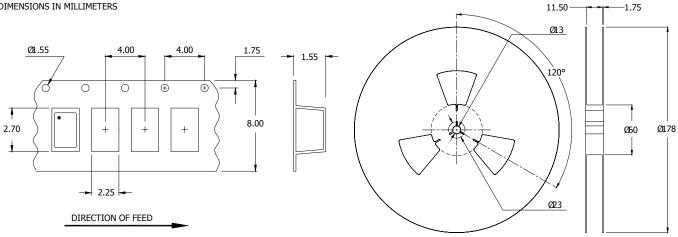
Model 425 2.5x2.0mm Low Cost Surface Mount Crystal

TABLE I

| | | MONTH | | | | FEB | MAR | APR | MAY | JUN | JUL | AUG | SEP | ост | NOV | DEC |
|------|------|-------|------|------|-----|-----|-------|-----|--------|------|-----|-----|-----|-----|-----|-----|
| | YEAR | | | | JAN | FLD | IVIAK | AFK | IVIA I | 2014 | 305 | AUG | JLF | 001 | NOV | DEC |
| 2001 | 2005 | 2009 | 2013 | 2017 | Α | В | С | D | E | F | G | Н | J | K | L | М |
| 2002 | 2006 | 2010 | 2014 | 2018 | Ν | Р | Q | R | S | Т | U | V | W | Х | Y | Z |
| 2003 | 2007 | 2011 | 2015 | 2019 | а | b | с | d | е | f | g | h | j | k | Ι | m |
| 2004 | 2008 | 2012 | 2016 | 2020 | n | р | q | r | S | t | u | v | w | х | У | z |

TAPE AND REEL INFORMATION

DIMENSIONS IN MILLIMETERS



Device quantity is 3,000 pieces mimimum per 178mm reel.

ENVIRONMENTAL SPECIFICATIONS

| Temperature Cycle: | 400 cycles from -55° C to $+125^{\circ}$ C, 10 minute dwell at each temperature, 1 minute transfer time between temperatures. |
|----------------------------------|--|
| Mechanical Shock: | 1,500g's, 0.5mS duration, V_2 sinewave, 3 shocks each direction along 3 mutually perpendicular planes (18 total shocks). |
| Sinusoidal Vibration: | 0.06 inches double amplitude, 10 to 55 Hz and 20g's, 55 to 2,000 Hz, 3 cycles each in 3 mutually perpendicular planes (9 times total). |
| Gross Leak: | No leak shall appear while immersed in an FC40 or equivalent liquid at +125°C for 20 seconds. |
| Fine Leak: | Mass spectrometer leak rates less than 2×10^{-8} ATM cc/sec air equivalent. |
| Resistance to Solder Heat: | Product must survive 3 reflows of +260°C peak, 10 seconds maximum. |
| High Temperature Operating Bias: | 2,000 hours at +125°C, disregarding frequency shift. |
| Frequency Aging: | 1,000 hours at +85°C, maximum \pm 5 ppm shift. |
| Insulation Resistance: | 500M Ohms @ $100V_{DC} \pm 15V_{DC}$. |
| Moisture Sensitivity Level: | Level 1 per JEDEC J-STD-020. |

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